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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/951,384 09/14/2001 PAT 6,682,963 *VERIFIED*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-280864 09/14/2000 *VERIFIED*  
 JAPAN 2000-280902 09/14/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 25	TOTAL CLAIMS 25	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Verified and Acknowledged Examiner's Signature <i>TMA</i> Initials <i>JD</i>				

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TITLE  
 Semiconductor device and manufacturing method thereof

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